

<b>Notice of References Cited</b>	Application/Control No. 10/660,697	Applicant(s)/Patent Under Reexamination CHAMNESS, KEVIN ANDREW	
	Examiner Jeffrey R. West	Art Unit 2857	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-2003/0055523	03-2003	Bunkofske et al.	700/108
*	B	US-2002/0107858	08-2002	Lundahl et al.	707/100
*	C	US-2005/0055175	03-2005	Jahns et al.	702/182
*	D	US-6,622,059	09-2003	Toprac et al.	700/121
*	E	US-5,796,606	08-1998	Spring, Robert A.	700/9
*	F	US-2003/0144746	07-2003	Hsiung et al.	700/28
*	G	US-5,949,678	09-1999	Wold et al.	700/83
*	H	US-6,896,763	05-2005	Balasubramhanya et al.	156/345.24
*	I	US-6,330,526	12-2001	Yasuda, Takeshi	703/2
*	J	US-6,675,137	01-2004	Toprac et al.	703/2
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	Cherry et al., "Semiconductor Process Monitoring and Fault Detection Using Recursive Multi-Way PCA", TWMCC February 19, 2002.
*	V	Shirazi et al., "A Modular Realization of Adaptive PCA" 1997 IEEE International Conference on Systems, Man, and Cybernetics, 1997. 'Computational Cybernetics and Simulation'. Volume 4, 12-15 Oct. 1997 Page(s):3053 - 3056.
*	W	Chatterjee et al., "Algorithms for Accelerated Convergence of Adaptive PCA" IEEE Transactions on Neural Networks, Volume 11, Issue 2, March 2000 Page(s):338 - 355.
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.